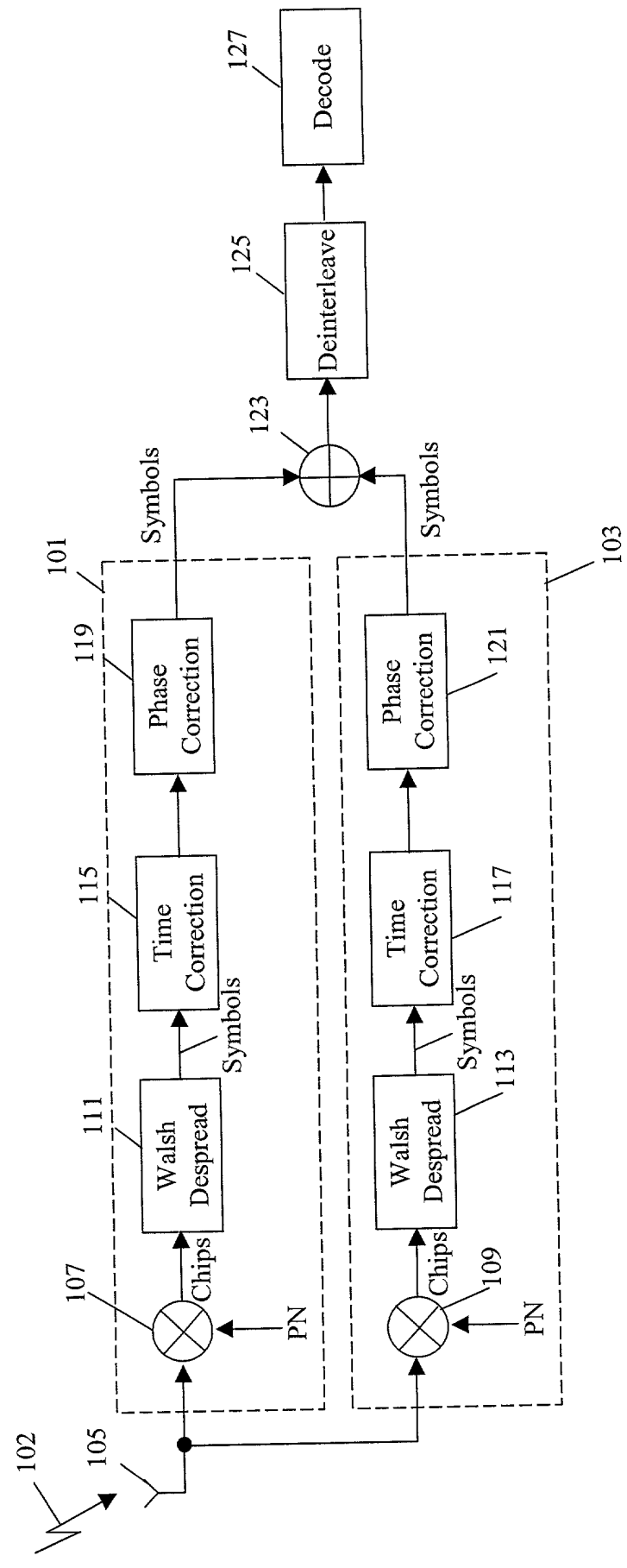


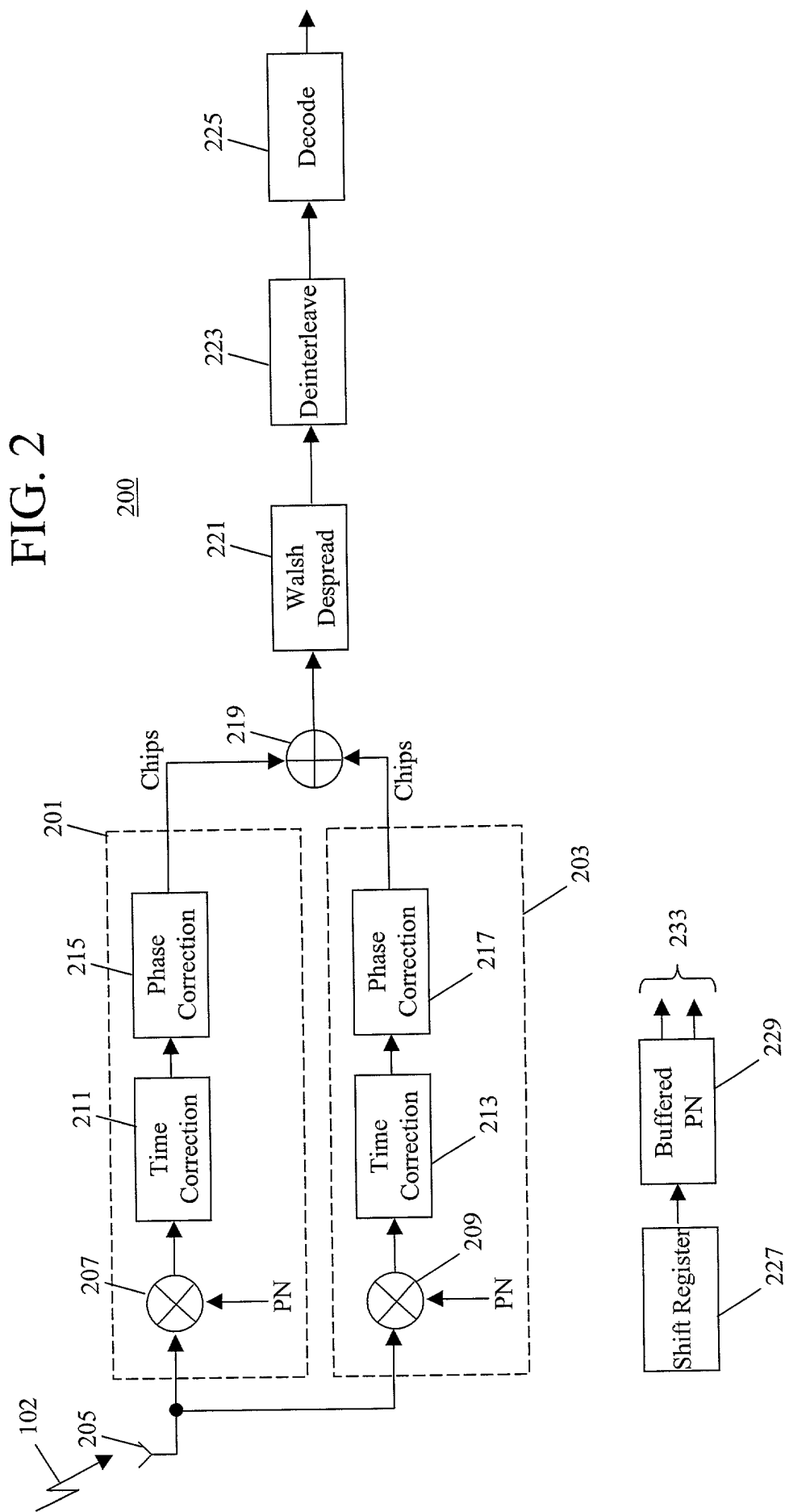
FIG. 1 is a block diagram of a communication system 100. The system 100 includes a transmitter 101 and a receiver 103. The transmitter 101 includes a Walsh Despread block 111, a Time Correction block 115, and a Phase Correction block 119. The receiver 103 includes a Walsh Despread block 113, a Time Correction block 117, and a Phase Correction block 121. The transmitter 101 also includes a PN block 107 and a Chips block 109. The receiver 103 also includes a PN block 109 and a Chips block 107. The transmitter 101 and receiver 103 are connected via a communication channel 105. The transmitter 101 outputs Symbols 123 to the receiver 103, which outputs Symbols 125 to the Decode block 127.

FIG. 1

Prior Art

100







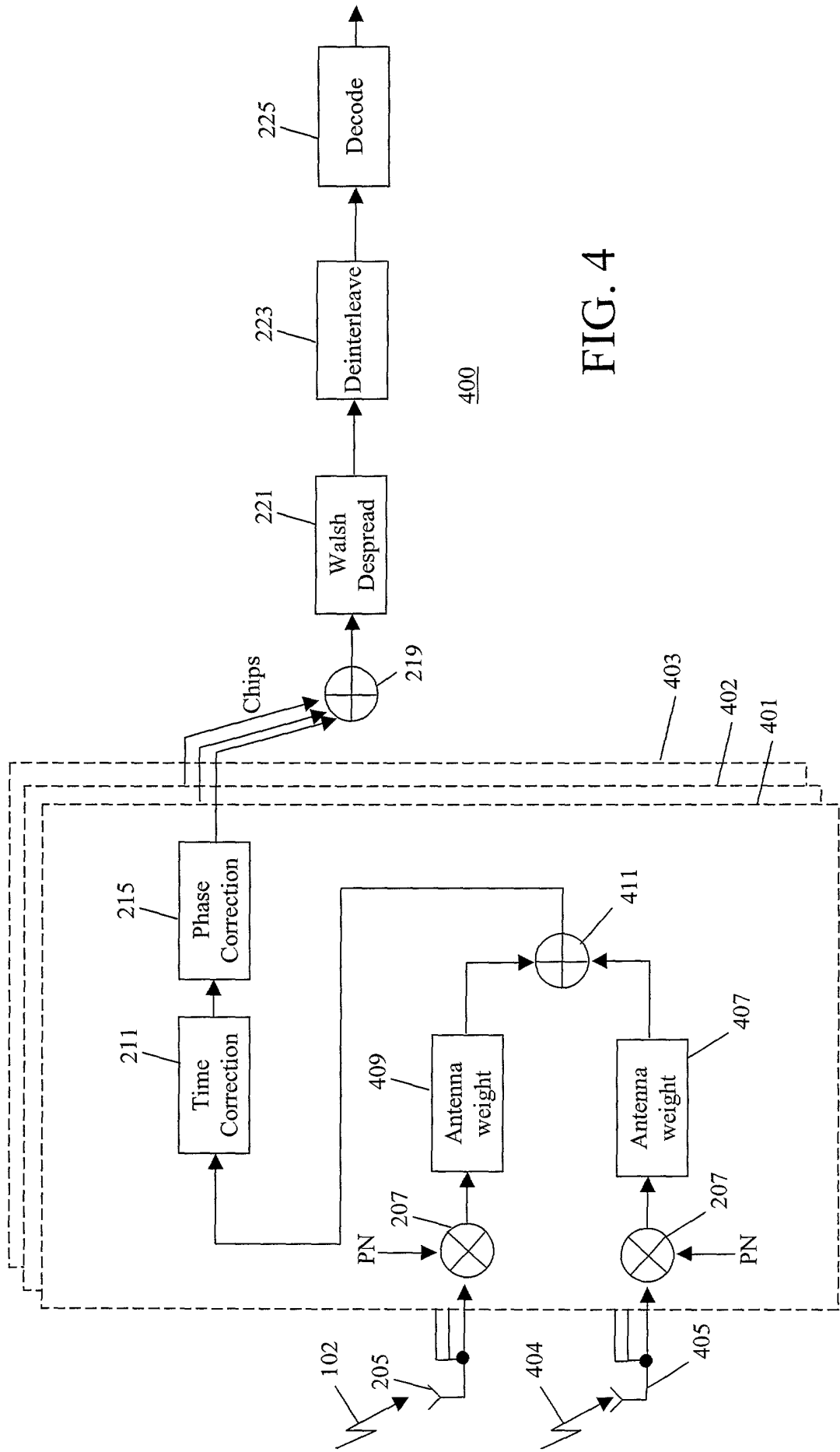


FIG. 4

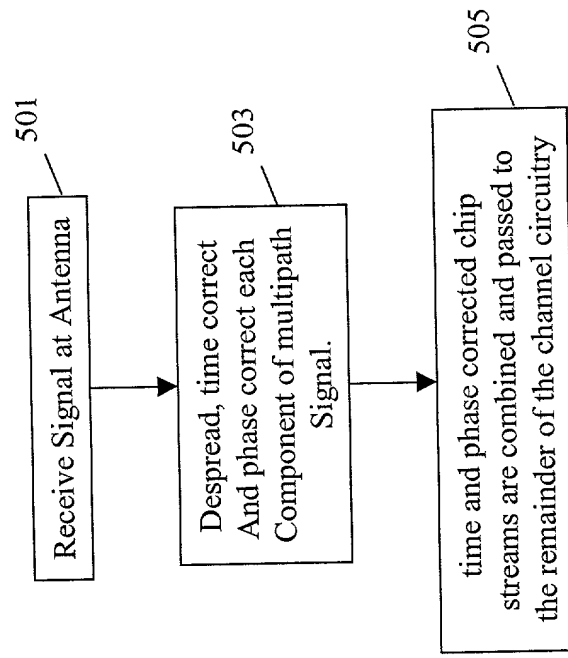


FIG. 5

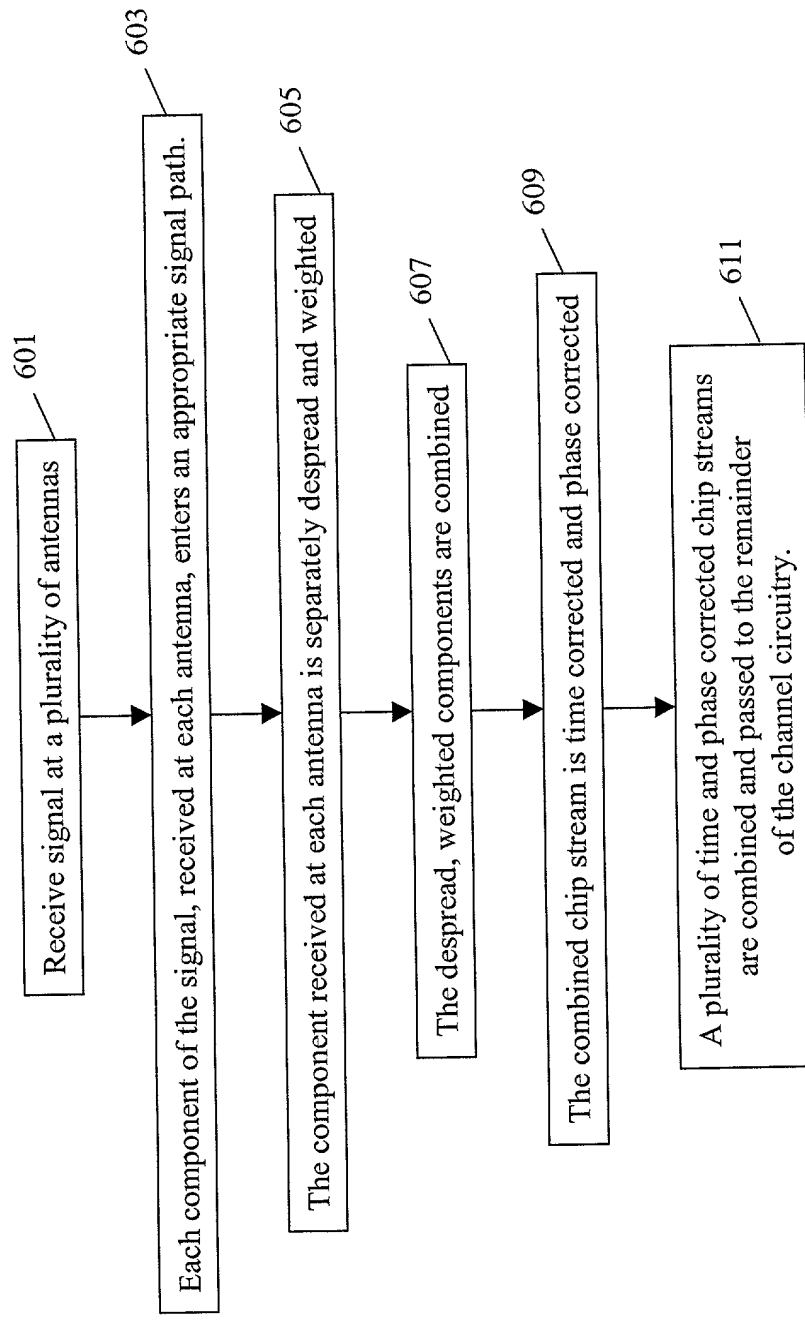
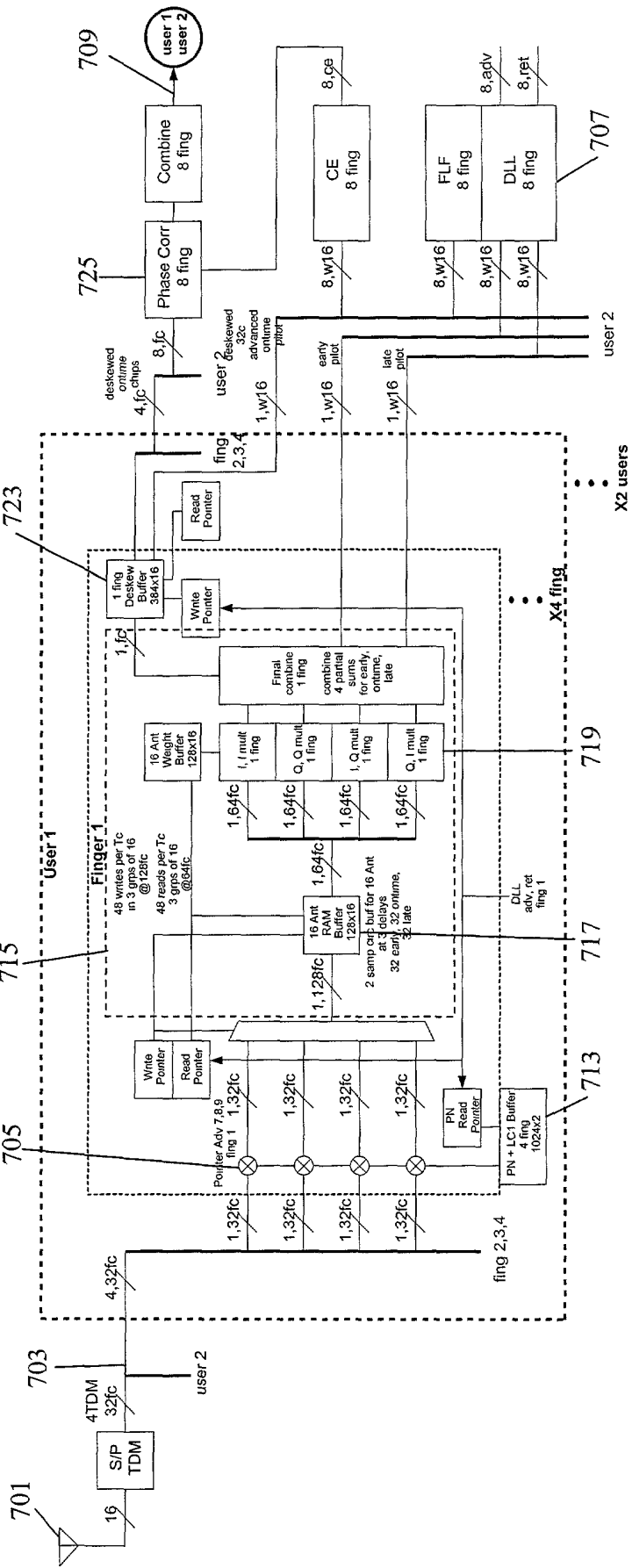


FIG. 6



700

FIG. 7